

FIG. 1

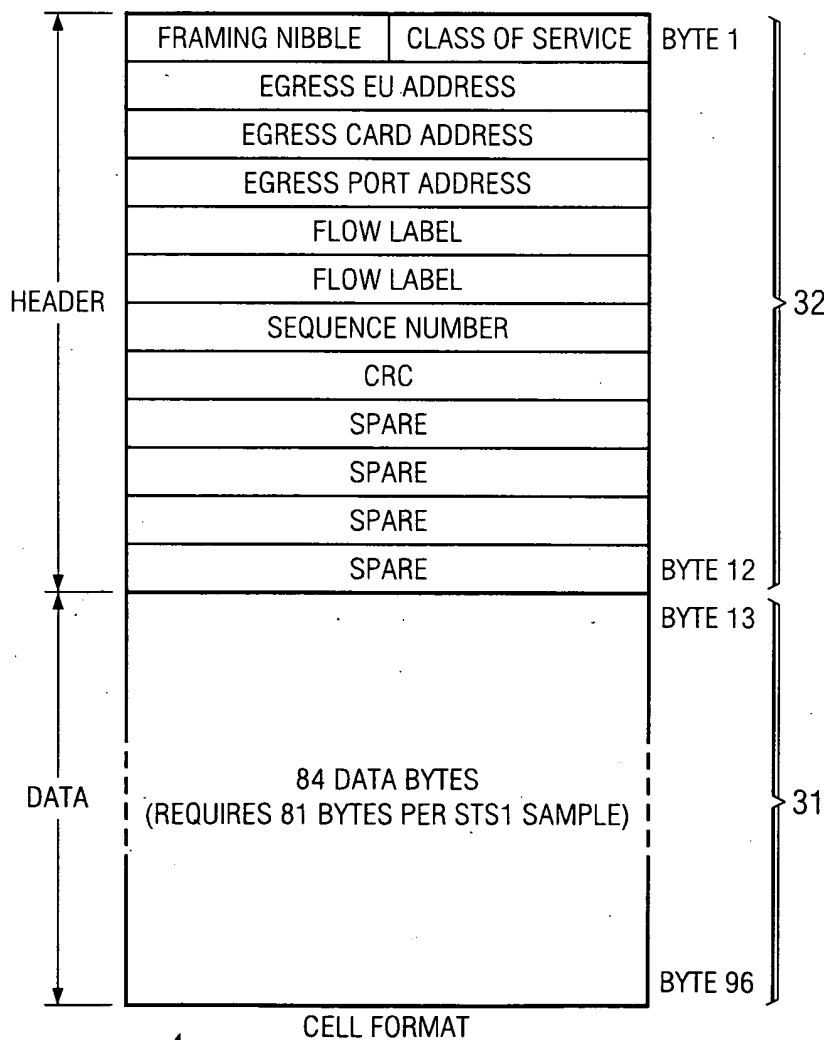


FIG. 3

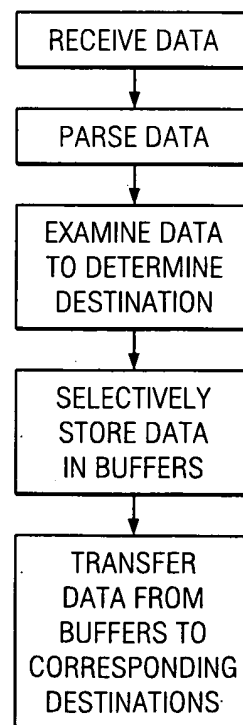


FIG. 9

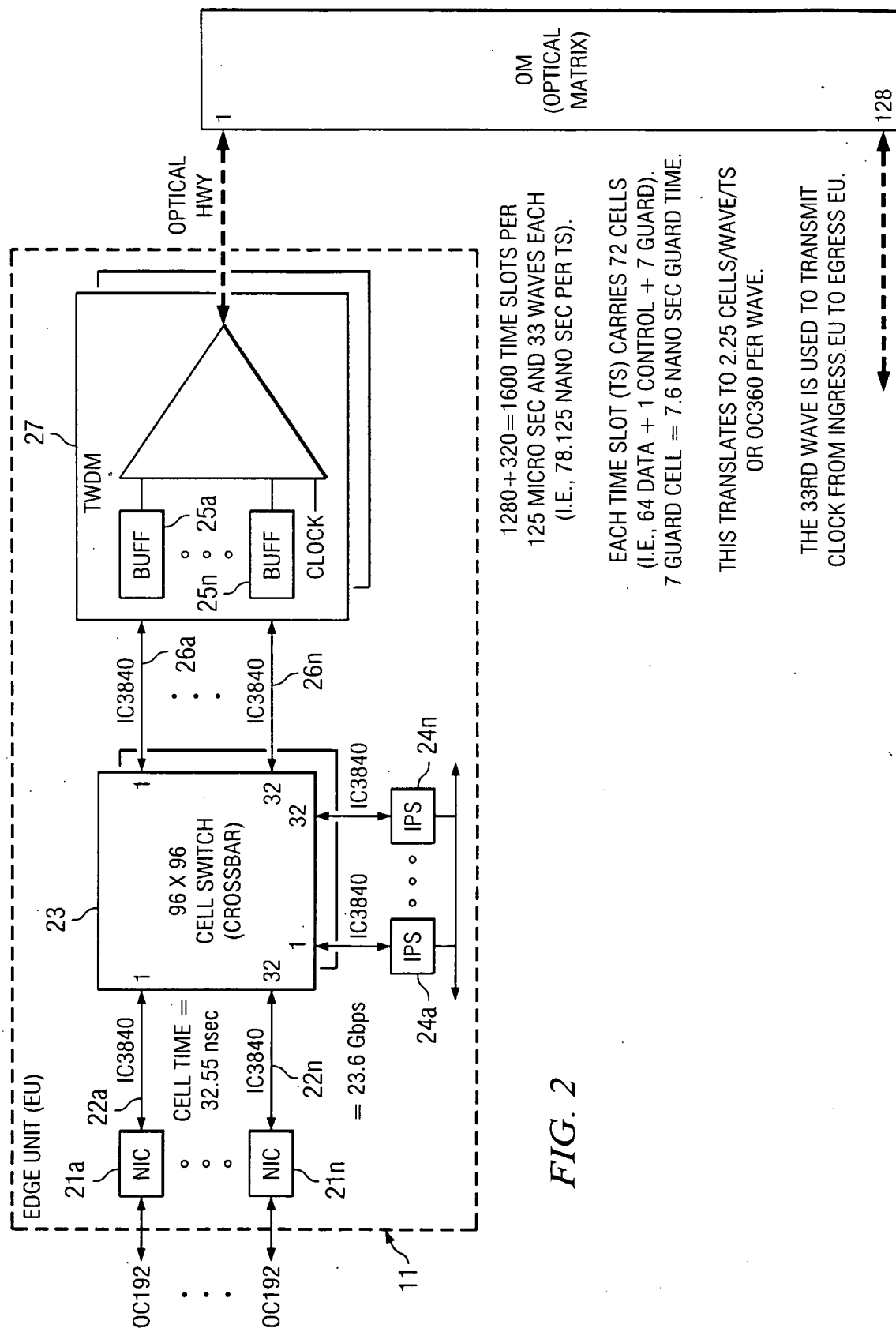
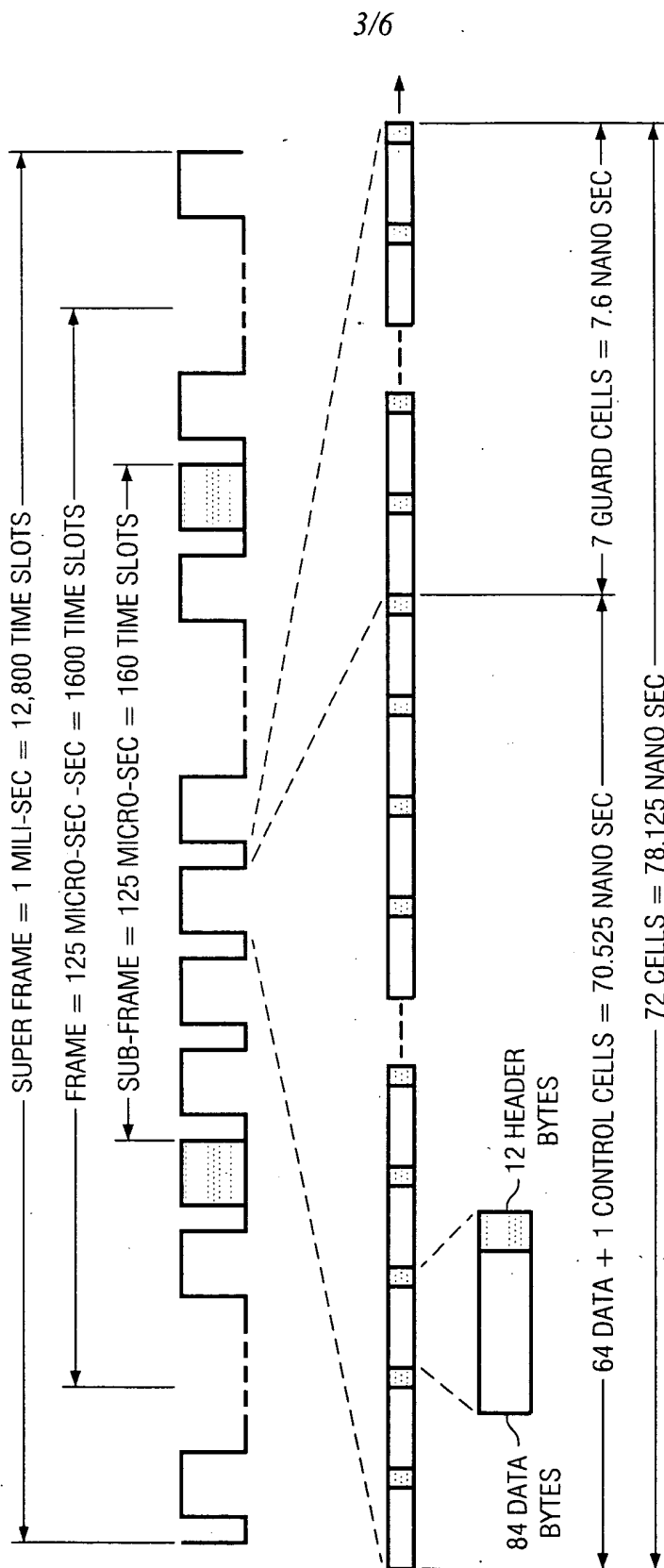


FIG. 2



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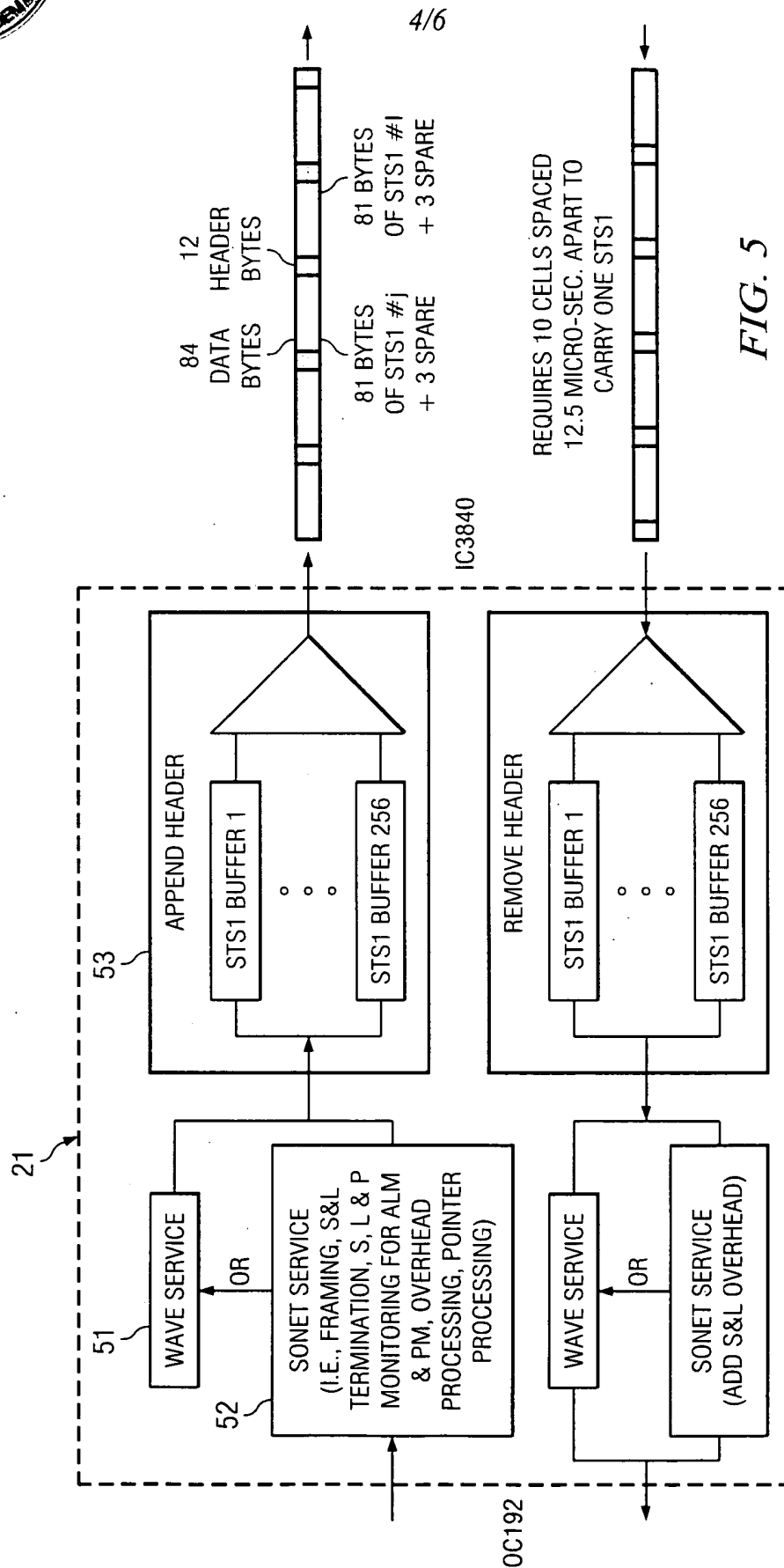
REPLACEMENT
SHEET 1





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REPLACEMENT
SHEET





YOTTA1250

REPLACEMENT
SHEET

96 × 96 CELL SWITCH(CS96)
(PREFER 128 × 128 CELL
SWITCH FOR FUTURE
SERVICES)

CELL TIME =
32.55 NANO-SEC.

CELL SIZE =
96 BYTES X 8 = 768 BITS

BIT TIME =
42.39 PICO-SEC.

SET UP THE CROSS CONNECTION
FOR 32.55 NANO-SEC. WHEN 768
BITS FLOW THROUGH.

THE OPTIONAL S/P LOGIC, REDUCES
THE BIT RATE THROUGH THE CELL
SWITCH, BUT INCREASES THE
NUMBER OF CELL SWITCH PLANES.

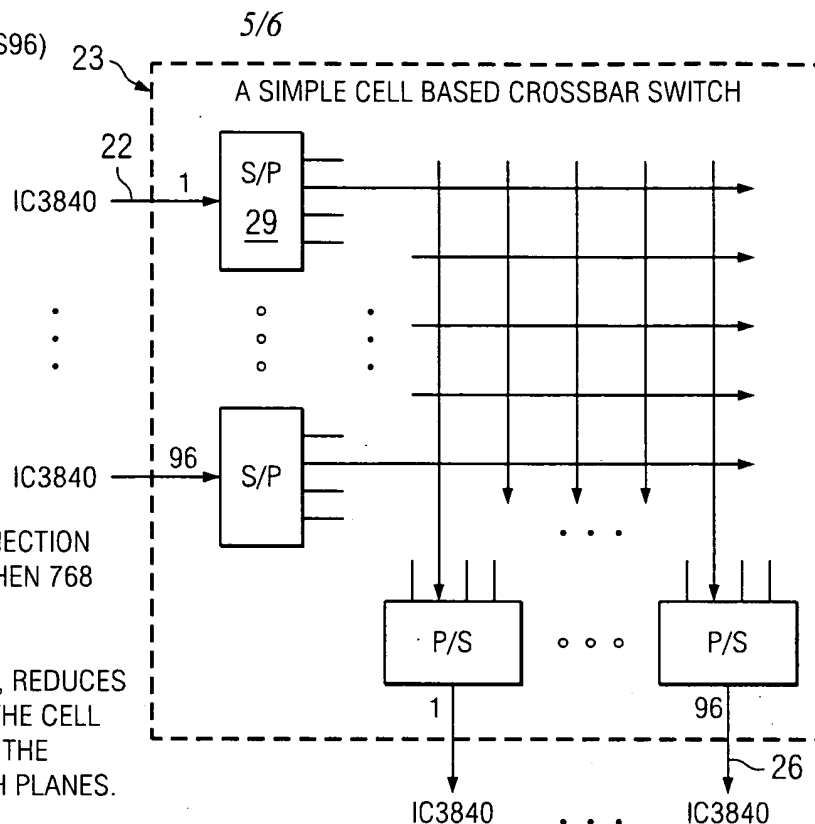


FIG. 6

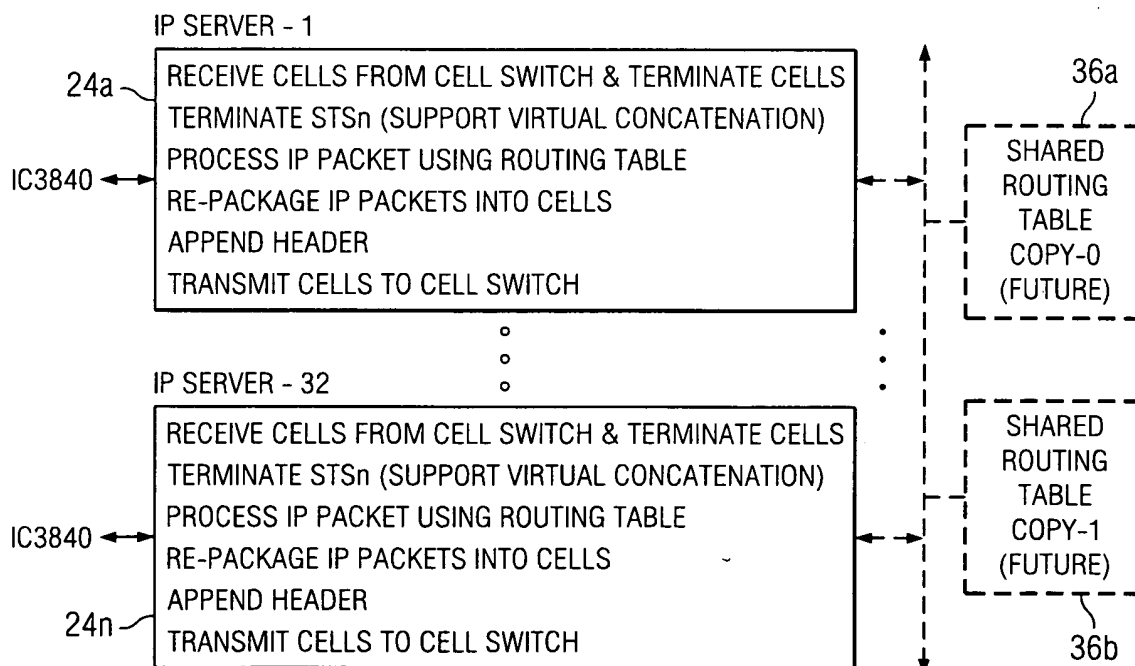
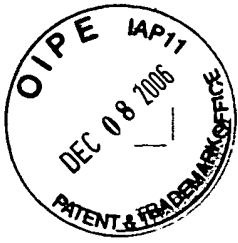


FIG. 8



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REPLACEMENT
SHEET

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